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LIST OF	REFER	ENCES CITED BY APPLICANT		Keiji HOSOTANI, et al.				
				FILING DATE		GROUP		
				July 10, 2003				
				U.S. PATENT DOCUMENTS				
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	R. SCHEUERLEIN, et al., ISSCC 2000 / Session 7 / TD: Emerging Memory & Device Technologies / Paper TA 72, pages 94-95. "TA 7.2: A 10NS READ AND WRITE NON-VOLATILE MEMORY ARRAY USING A MAGNETIC TUNNEL							
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Examiner andy Muya				Q	Date Considered 2/6/04			
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								